

L Number	Hits	Search Text	DB	Time stamp
-	1	((711/3,5,118-129,202).CCLS.) and instruction and (address adj (conver\$4 decode\$1 control\$2))) and (load adj module)	USPAT; US-PGPUB	2002/05/06 11:29
-	0	("l1 and ((dual multiple) adj memory adj management)").PN.	USPAT; US-PGPUB	2002/05/06 11:43
-	2	((711/3,5,118-129,202).CCLS.) and ((dual multiple) adj memory adj management)	USPAT; US-PGPUB	2002/05/06 11:47
-	38	((711/3,5,118-129,202).CCLS.) and ((dual multiple) with (memory adj management))	USPAT; US-PGPUB	2002/05/06 11:48
-	25	((711/3,5,118-129,202).CCLS.) and (instruction adj cache)) and @ad <= "19991104") and SDRAM	USPAT; US-PGPUB	2002/05/06 16:36
-	6838	(dual two many multi multiple plural plurality) with ((memory adj (control controller manager management)) (address adj ( convert\$2 conversion translate translator))))	USPAT; US-PGPUB	2002/05/06 16:43
-	6223	((dual two many multi multiple plural plurality) with ((memory adj (control controller manager management)) (address adj ( convert\$2 conversion translate translator)))) and @ad <= "19991104"	USPAT; US-PGPUB	2002/05/06 16:44
-	2671	((dual two many multi multiple plural plurality) with ((memory adj (control controller manager management)) (address adj ( convert\$2 conversion translate translator)))) and @ad <= "19991104") and instruction	USPAT; US-PGPUB	2002/05/06 16:44
-	0	((dual two many multi multiple plural plurality) with ((memory adj (control controller manager management)) (address adj ( convert\$2 conversion translate translator)))) and @ad <= "19991104") and instruction) and SDRAM) and (logical adj address) and (phisical adj address)	USPAT; US-PGPUB	2002/05/06 16:47
-	676	((dual two many multi multiple plural plurality) with ((memory adj (control controller manager management)) (address adj ( convert\$2 conversion translate translator)))) and @ad <= "19991104") and instruction) and DRAM	USPAT; US-PGPUB	2003/06/26 20:42
-	4380	(711/3,5,118-129,202).CCLS.	USPAT; US-PGPUB	2003/06/26 19:54
-	21	((711/3,5,118-129,202).CCLS.) and instruction and (address adj (conver\$4 decode\$1 control\$2))) and SDRAM) and @ad <=19991104	USPAT; US-PGPUB	2003/06/26 19:55
-	0	maruayma	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/26 20:20
-	34347	maruyama	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/26 20:20
-	0	2001/0003199	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/26 20:20
-	2	"20010003199"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/26 20:22

-	16	"6173381"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/26 20:23
-	30	((((dual two many multi multiple plural plurality) adj ((memory adj (control controller manager management)) (address adj (convert\$2 conversion translate translator)))) and @ad <= "19991104") and instruction) and DRAM	USPAT; US-PGPUB	2003/06/26 20:34
-	527	((memory adj (control controller manager management)) and ((address near (convert\$2 conversion translate\$3)) with physical with (logical virtual))) and @ad <= "19991104") and (address with compar\$4)	USPAT; US-PGPUB	2003/06/26 20:52
-	951	((memory adj (control controller manager management)) and ((address near (convert\$2 conversion translate\$3)) with physical with (logical virtual))) and @ad <= "19991104"	USPAT; US-PGPUB	2003/07/12 16:29
-	292	((memory adj (control controller manager management)) and ((address near (convert\$2 conversion translate\$3)) with physical with (logical virtual))) and @ad <= "19991104") and (address with comparator)	USPAT; US-PGPUB	2003/07/12 16:30
-	978	((711/3,5,118-129,202).CCLS.) and (instruction adj cache)	USPAT; US-PGPUB	2003/07/12 17:51
-	47	((711/3,5,118-129,202).CCLS.) and instruction and (address adj (conver\$4 decode\$1 control\$2))) and SDRAM	USPAT; US-PGPUB	2004/01/12 10:45
-	23	((711/3,5,118-129,202).CCLS.) and instruction and (address adj (conver\$4 decode\$1 control\$2))) and SDRAM) and @ad <19991104	USPAT; US-PGPUB	2004/01/12 10:46
-	15	((((dual two many multi multiple plural plurality) with ((memory adj (control controller manager management)) (address adj (convert\$2 conversion translate translator)))) and @ad <= "19991104") and instruction) and SDRAM) and (logical adj address)	USPAT; US-PGPUB	2004/01/12 10:48
-	144	((((dual two many multi multiple plural plurality) with ((memory adj (control controller manager management)) (address adj (convert\$2 conversion translate translator)))) and @ad <= "19991104") and instruction) and SDRAM	USPAT; US-PGPUB	2004/01/12 10:49
-	751	((711/3,5,118-129,202).CCLS.) and (instruction adj cache)) and @ad <= "19991104"	USPAT; US-PGPUB	2004/01/12 10:51
-	677	((711/3,5,118-129,202).CCLS.) and instruction and (address adj (conver\$4 decode\$1 control\$2))	USPAT; US-PGPUB	2004/01/12 10:52
-	555	((711/3,5,118-129,202).CCLS.) and instruction and (address adj (conver\$4 decode\$1 control\$2))) and @ad < "19991104"	USPAT; US-PGPUB	2004/01/12 10:52
-	84	((memory adj (control controller manager management)) and ((address near (convert\$2 conversion translate\$3)) with physical with (logical virtual))) and @ad <= "19991104") and (address near comparator)	USPAT; US-PGPUB	2004/01/12 10:53